

A world map where the Americas (North and South America) are filled with a colorful, abstract pattern resembling a circuit board or data visualization. The rest of the world is a solid orange color. The map is partially obscured by blue geometric shapes in the foreground.

Vendor Update – Intel 49th IDC HPC User Forum

Mike Lafferty
HPC Marketing
Intel Americas Corp.

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*In Next 5 Years, Make Cloud Security Equal to or Better Than
Traditional Best In Class Enterprise Security*



Available Today

Secure Cloud Data Centers

Intel VT & TXT, McAfee MOVE AV, McAfee ePO¹, Application Control

Secure the Connections

McAfee Cloud Security Platform

Secure User & Intelligent
Devices

*Intel Identity Protection Tech., McAfee Cloud Identity Manager, McAfee
Deep Defender*

Industry Collaboration

*Accelerate Broad Adoption of Security Standards for Cloud &
Enable Broad Range of Open, Interoperable Security Solutions*

Hardware-enhanced Security + Software & Services Key To Achieve Mission

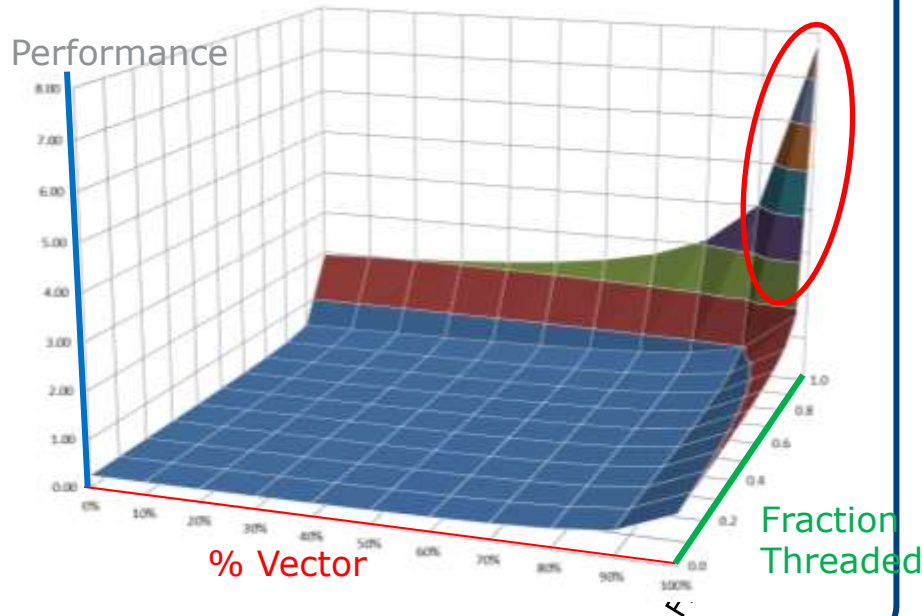
www.intel.com/cloudsecurity www.mcafee.com/datacenter

REDEFINING WHAT'S POSSIBLE – TOGETHER.

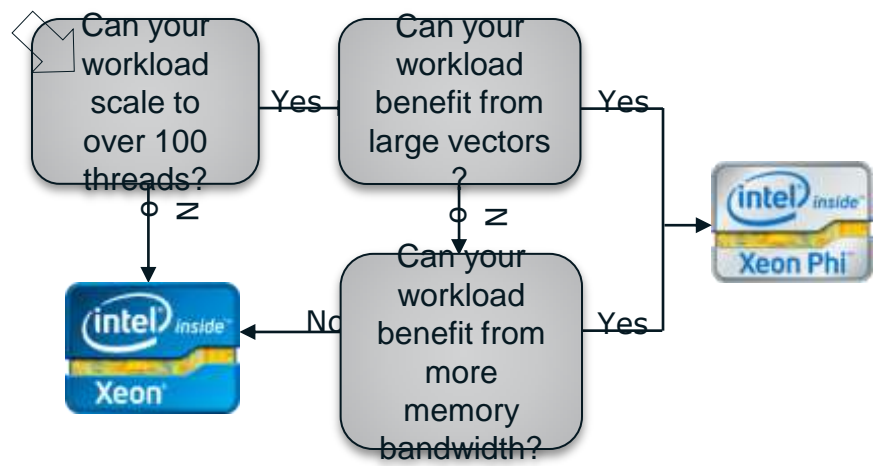
1 Integrating McAfee ePolicy Orchestrator (ePO) with Intel TXT requires custom integration work

Intel® Xeon Phi™ Coprocessors Workload Suitability

Suitability



* Theoretical acceleration of a highly parallel processor over an Intel® Xeon® parallel processor



If application scales with threads and vectors or memory bandwidth
→ Intel® Xeon Phi™ Coprocessors

Software, Drivers, & Online Resources Intel® Xeon Phi™ Coprocessor

Intel® Developer Zone
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Parallel Processing

Architecture for Discovery

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Xeon Phi

OVERVIEW | TOOLS & DOWNLOADS | PROGRAMMING | TRAINING | CASE STUDIES | ARTICLES/FORUMS/BLOGS

Productivity via architecture innovation coupled with familiar software.

Intel® Xeon Phi™ coprocessor:

- Extends hardware support to higher degrees of parallelism with power savings
- Uses familiar and standard programming models to preserve investments
- Shares parallel programming with general purpose processor

Learn how your applications can benefit from Intel Xeon Phi coprocessors:

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- An Overview of Programming for Intel® Xeon® processors and Intel® Xeon Phi™ coprocessors
- Intel® Xeon Phi™ Coprocessor Developer's Quick Start Guide
- Intel® Xeon Phi™ Coprocessor Instruction Set Architecture Reference Manual

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- Intel® Many Integrated Core Architecture Forum
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SW Development Tools & Programming Models

Parallel Processing
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VIDEO WORKSHOPS AND TUTORIALS CODE SAMPLES BOOKS EXTERNAL TRAINING

Software Developer Workshop Videos

A technical guide to the software development environment for the Intel® Xeon Phi™ coprocessor

- Beginning Workshops
- Advanced Workshops
- Tools Workshops
- Webinars

Intel® Software Events

Featured Webinar:
Intel® System Studio: Complete Development Solution for Intel® Many Integrated Core System
Watch IBM - Application news

Participate in engaging events that show how your software can take advantage of the latest processor technologies using new software development tools.

Date	Event	Location	Description	More Info
March 25	Intel® System Studio Complete Development Solution for Intel® Many Integrated Core System Webinar	Online	Intel® System Studio enables embedded developers to accelerate their time-to-market, streamline system integration, and boost power efficiency and performance.	LEARN MORE
March 22	Change the Game: Best Practice in Deploying and Managing HPC: Cluster with Intel® Xeon Phi™	Online	An IBM sponsored session featuring James VanDerBruggen of Intel Corporation, a software in-process from SCA, and a Product Line Executive from IBM	LEARN MORE

Check the "TRAINING" tab and "Events" link on <http://software.intel.com/mic-developer>



Getting Help On Programming – Intel® Xeon Phi™ Coprocessor

1. Intel® Developer Zone

- Start at <http://software.intel.com/mic-developer>
- Go to “Get Support” to get to the Intel® MIC Forums
- Search for postings related to issue(s) or create new posting for your issue

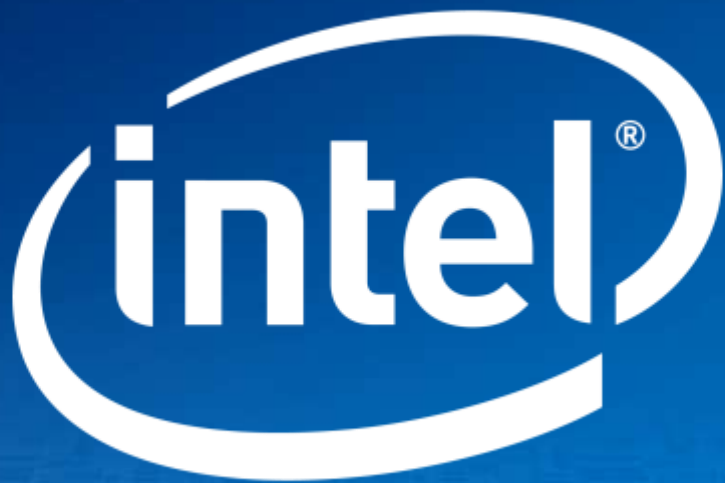
2. Intel® Premier Support

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- Available to users who have active licenses of Intel® software developer tools
- Submit questions, problems, tech support issues
 - Issues get examined in 4 business hours
- Monitor previously submitted issues

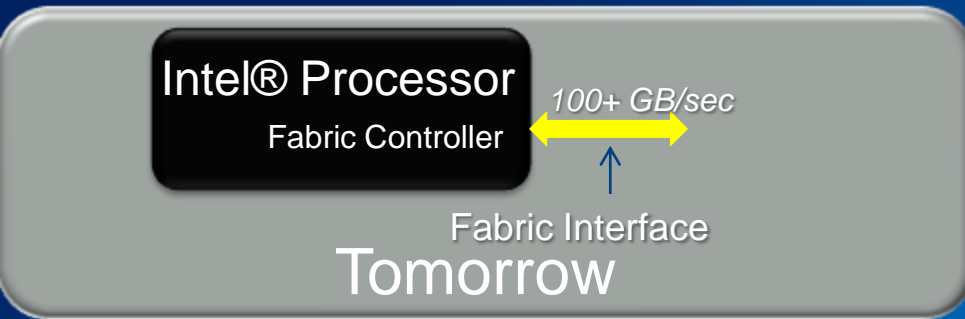
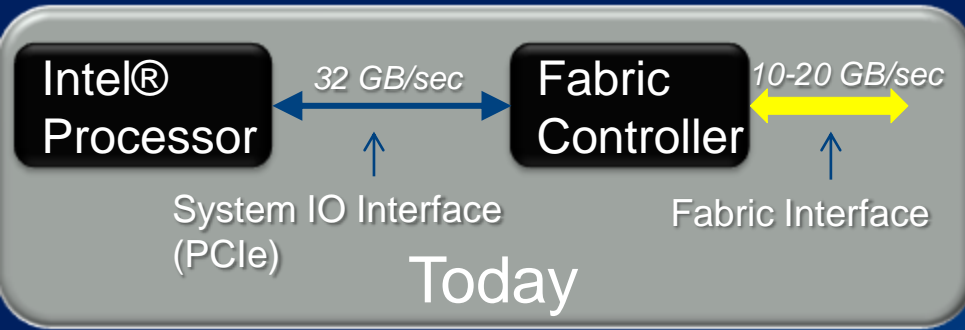
Please Note: For platform-related issues, contact your OEM

Check the “GET SUPPORT” tab on
<http://software.intel.com/mic-developer>





The Advantages of Fabrics Integration

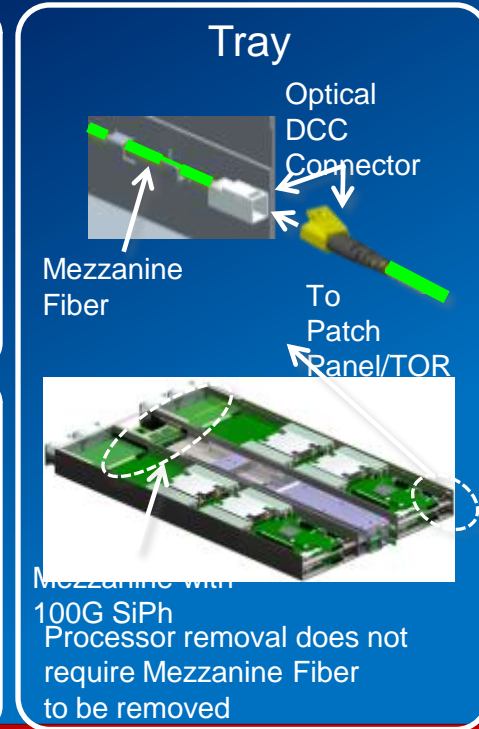
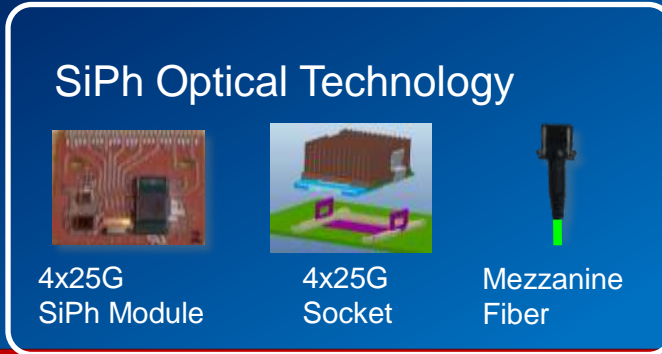
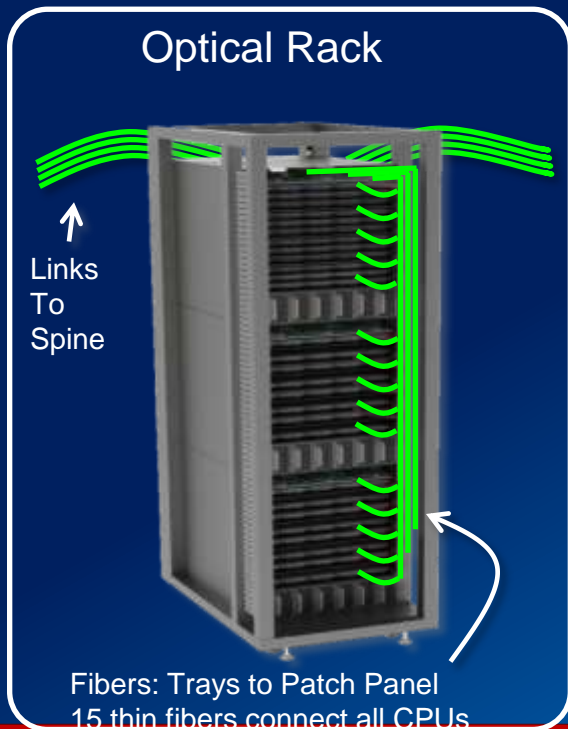


- Problem:
 - **Power** – System IO Interface Adds “10s Of Watts” Incremental Power
 - **Cost & Density** – More Components On A Server Node
 - **Scalability** – Processor Capacity & Fabric Bandwidth Scaling Faster Than System IO Bandwidth
- Solution:
 - Removing The System IO Interface From The Fabrics Solution **Reducing Power**
 - An Integrated Fabrics Results In **Fewer Components On The Server Node**
 - An Integrated Fabric **Balances Fabric and Compute, Scaling Application Performance & Efficiency**

Fabrics Integration Required to Scale Performance & Power



Rack Scale Architecture



Rack is configured once, ready for multi-generational use

Converged Storage Servers Support Scale-out Architecture

Achieves Greater Deployment Flexibility:

- Performance & Efficiency
- Availability
- Management
- Capacity

Storage Components

- Enclosure
- Backplane
- SATA/SAS Cables
- IOC/HBA
- Solid State Storage
- Hard Disks

Converged storage server

Server Components

- Server board
- Server chipset
- Server processor
- Server memory
- Network card
- Power supply

Metadata
Server

Application
Server

Storage
Server

Converged storage servers deliver a cost-effective scale-out storage platform

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